



Reconfigurable Computing Projects (Balster, Scarpino)



- VTC acceleration with FPGAs (2000)
- Rapid Prototyping System (RPS) (2004)
- P-Frame simplification (2005) *Predator transition*
- JPEG2000 compression (2005)
 - JPIP image communication protocol (2007) *AF transition*
 - FPGA compression acceleration (2008) *AF transition*
 - FPGA decompression acceleration (2009) *AF transition*
- DREAM board development (2006) *AFRL/RY transition*
- Mixed Signal Design Workbench (2008)
- Non-linear Equalization of A/D converters (2009)
- A/D converter, mixed signal modeling and sim. (2009)