

## **Electro-Optics Seminar**

**Friday, November 11, 2016 at 3:30 PM in FH 580**

# **The Bridge to Obtaining a US Patent Charge Transport in Organic and Inorganic Semiconductors: a Case Study**

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### **Abstract**

In this presentation we shall explore the stages a patent application go through from the inception of an idea, to writing and submitting the disclosure as well as the examination process that a patent application undergoes until the patent is issued. We shall describe what constitutes “prior art” in view of the state of art information available in the area thought for obtaining a patent. We shall briefly review the legal statutes that a patent must comply with and the important time periods for applying for a patent.

### **Brief Biography**

Yasser Abdelaziez is a primary examiner at the United States Patent and Trademark Office (USPTO), Semiconductors group. He earned his Master degree from NJIT in EE with emphasis on Microelectronics and a thesis subject of micro-mirror arrays for optical switching, and a PhD degree in ECE from UD with emphasis on the fabrication and optical characterization of EO adaptive microlens arrays and optical beam shaping and control using AO techniques. After his PhD, he joined University of British Columbia Physics Dept. as a postdoctoral fellow where he conducted R&D in the area of electronic papers. Subsequently, he joined American Science and Technology in Brookings, SD as a manager for MEMS development program for defense applications. Besides working on R&D, he had experience with project management and was involved in several SBIR submissions in the area of MEMS sensors and optical switching application. During his research years, he has authored and co-authored 6 journal and 10 conference papers focusing on adaptive optics, MEMS and capillary electrophoresis. He joined the USPTO 2008 as a patent examiner in the area of semiconductor devices and their fabrication methods, and was promoted to a primary examiner level 2013.